

M V R College of Engineering and Technology (MVCET)

1st International Conference on Microstructure, VLSI, Robotics, Communication, Electrical & Emerging Technologies using AI-ML Algorithms (ICMVCET - 2025)



About the conference: The International Conference on Microstructure, VLSI, Robotics, Communication, Electrical & Emerging Technologies using AI-ML Algorithms (MVCET) is a premier global event. It brings together researchers, engineers, and industry experts to share their latest innovations and findings. The conference focuses on the application of AI-ML algorithms in various fields, exploring their potential to transform traditional technologies and drive future advancements. The conference features several technical tracks, each focusing on a specific area of research. The conference includes paper presentations and poster sessions, where researchers and engineers can share their latest research findings and innovations. A technical program committee reviews all submissions to ensure that only high-quality papers are presented. Networking opportunities allow attendees to connect with peers, establish new collaborations, and explore potential partnerships. An exhibition featuring companies and organizations showcasing their latest products, services, and technologies is also a key part of the conference.

About the host Institute: M.V.R. College of Engineering and Technology (MVCET) is a renowned technological institution in Paritala, Vijayawada, Andhra Pradesh. Established in 2008, the college is affiliated with Jawaharlal Nehru Technological University, Kakinada (JNTUK), and approved by the All India Council for Technical Education (AICTE). MVCET offers undergraduate and postgraduate programs in engineering, including Diploma, B.Tech, M.Tech, MBA, and MCA in various disciplines, and MBA & MCA programs. The institution boasts state-of-the-art infrastructure and research facilities, including well-equipped laboratories, modern workshops, and an extensive library. MVCET encourages research and development activities among its faculty and students and has established research centers and collaborations with industries and institutions. The institution also has a dedicated placement cell that provides training and placement opportunities to students, with tie-ups with top companies such as Infosys, Wipro, TCS, HCL.

Technical Tracks

-  Computational intelligence, AI and ML Algorithms.
-  VLSI, Signal & Image Processing, CS, IoT.
-  Micro & Nano Materials, Devices & Systems, VLSI.
-  EV, EV Infra, Power Systems, Control Systems, Energy.
-  Mechanical, Robotics & Automation.
-  Civil, Infrastructure & Smart Systems.

Important Dates

- Submission Deadline : Feb 15, 2025
- Acceptance Notification: Feb 18, 2025 (onwards)
- Registration Deadline : Feb 20, 2025
- Conference Dates : March, 21 - 22, 2025

Paper Submission

Conference Website: <https://icmvcet2025.in>

Paper Format: <https://shorturl.at/15kPm>

CMT Link (Paper submission):

<https://cmt3.research.microsoft.com/ICMVR>

Registration Link (After acceptance):

<https://shorturl.at/D8vWg>

Registration Charges

Type of Registration	Indian Author	Foreign Author
Industry Participants	12,000/-	USD 350
Academicians / Research Scholars	10,000/-	USD 300
U G & PG Students	9,500/-	USD 200

Submission guidelines

The full paper must be submitted through the platform Microsoft Conference Management Tool Kit (CMT) Each author confirms that he is aware of the Publication Ethic & Malpractice Statement and the Privacy Policy of the event. Authors are expected to have Scope, Novelty, Validity, data reported, analyzed, and interpreted correctly, Clarity, Compliance, and significant contribution in their articles and must meet the guidelines of format. The maximum number of pages is allowed only Five (5).

Review Process & Indexing

The full paper must be submitted through CMT or icmvcet2025@mvrcoe.ac.in Final papers with Plagiarism of less than 10% undergo a review process. All the Accepted and Presented papers will be included in Taylor & Francis Conference proceedings, indexed in SCOPUS.

Payment Details

Account Number: 141213500009470
 IFSC Code: KVBL0001412,
 Name: EISPL, KVB Bank, Tirupati - 517501.
 Payment Direct : 9959408716@ICICI



Venue

MVR College of Engineering and Technology, NH9, paritala, Ibrahimpattam, Vijayawada, Andhra Pradesh, India - 521180.



International Keynote speakers



Dr. Jabin Geevarghese George,
 Digital Transformation Architect, TCS
 Bayonne, New Jersey, USA



Dr. Arijit Bhattacharjee
 Research Fellow,
 National University of Singapore



Dr. Bhavani Prasad Yalagala
 Research Manager
 University of Glasgow, U.K

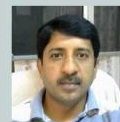
National Keynote speakers



Dr. Om Jee Pandey
 Indian Institute of Technology (BHU) Varanasi



Dr. Vimal Kumar Singh Yadav,
 Asst professor, Dept of ECE,
 MNNIT, Allahabad



Prof. U S N Raju, PhD
 Assoc. Prof, Dept of CSE
 NIT, Warangal



Dr. Satyam agarwal,
 Asst professor,
 Dept of ECE, IIT Ropar

Contact Person

B. Kiran Kumar
 Contact No: 99594 08716
 E-Mail: icmvcet2025@mvrcoe.ac.in